

l4_lp_space (TMWddCx- uvj1e4VpD4mcwz9oNF8LnfYcmxoz)

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Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_numbers : \iota$ be given. Let $r1_xreal_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k6_numbers : \iota$ be given. Let $k4_power : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k8_real_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_xreal_0 : \iota \Rightarrow o$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $v2_xxreal_0 : \iota \Rightarrow o$ be given. Let $v3_xxreal_0 : \iota \Rightarrow o$ be given. Let $k1_xboole_0 : \iota$ be given. Let $v1_xcmplx_0 : \iota \Rightarrow o$ be given. Let $k3_xcmplx_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $np_1 : \iota$ be given. Let $k3_power : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $m2_subset_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k5_numbers : \iota$ be given. Let $np_0 : \iota$ be given. Let $v1_xxreal_0 : \iota \Rightarrow o$ be given. Let $v3_membered : \iota \Rightarrow o$ be given. Let $k9_prepower : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_int_1 : \iota \Rightarrow o$ be given. Let $k4_prepower : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $v1_membered : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow ((r1_xreal_0 X0 X1) \Rightarrow ((v1_xboole_0 X0) \vee ((v2_xxreal_0 X1) \vee (v3_xxreal_0 X0)))))) \quad (1)$$

Assume the following.

$$\forall X0.(v1_xboole_0 X0) \Rightarrow (X0 = k1_xboole_0) \quad (2)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow ((r1_xreal_0 X0 X1) \Rightarrow ((v1_xboole_0 X1) \vee ((v3_xxreal_0 X0) \vee (v2_xxreal_0 X1)))))) \quad (3)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(r1_xreal_0 X0 X1) \wedge ((\neg v2_xxreal_0 X1) \wedge (v2_xxreal_0 X0)))) \quad (4)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(r1_xreal_0 X0 X1) \wedge ((\neg v3_xxreal_0 X0) \wedge (v3_xxreal_0 X1)))) \quad (5)$$

Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k3_xcmplx_0 \text{ np_1 } X0 = X0) \quad (6)$$

Assume the following.

$$\begin{aligned} \forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\forall X2. \\ (v1_xreal_0 X2) \Rightarrow (\neg(\neg r1_xreal_0 X0 \text{ k6_numbers}) \wedge ((\neg r1_xreal_0 \\ X1 \text{ k6_numbers}) \wedge (k3_power (k3_xcmplx_0 X0 X1) X2 \neq k3_xcmplx_0 (\\ k3_power X0 X2) (k3_power X1 X2)))))) \end{aligned} \quad (7)$$

Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k3_xcmplx_0 X0 \text{ k6_numbers} = \text{k6_numbers}) \quad (8)$$

Assume the following.

$$\begin{aligned} ((v2_xxreal_0 \text{ np_1}) \wedge (m2_subset_1 \text{ np_1 } k1_numbers \text{ k5_numbers})) \wedge \\ ((m1_subset_1 \text{ np_1 } k5_numbers) \wedge (m1_subset_1 \text{ np_1 } k1_numbers)) \end{aligned} \quad (9)$$

Assume the following.

$$(m2_subset_1 \text{ np_0 } k1_numbers \text{ k5_numbers}) \wedge ((m1_subset_1 \text{ np_0 } k5_numbers) \wedge (m1_subset_1 \text{ np_0 } k1_numbers)) \quad (10)$$

Assume the following.

$$v1_xboole_0 \text{ np_0} \quad (11)$$

Assume the following.

$$\forall X0.\forall X1.((m1_subset_1 X0 \text{ k1_numbers}) \wedge (v1_xreal_0 X1)) \Rightarrow (k8_real_1 X0 X1 = k3_xcmplx_0 X0 X1) \quad (12)$$

Assume the following.

$$k6_numbers = k1_xboole_0 \quad (13)$$

Assume the following.

$$\forall X0.\forall X1.((m1_subset_1 X0 \text{ k1_numbers}) \wedge (m1_subset_1 X1 \text{ k1_numbers})) \Rightarrow (k4_power X0 X1 = k3_power X0 X1) \quad (14)$$

Assume the following.

$$\exists X0.(v1_xboole_0 X0) \wedge (v1_xxreal_0 X0) \quad (15)$$

Assume the following.

$$\exists X0.(v1_xboole_0 X0) \wedge ((v1_xcmplx_0 X0) \wedge ((v1_xreal_0 X0) \wedge (v1_xreal_0 X0))) \quad (16)$$

Assume the following.

$$\exists X0.(v1_xcmplx_0 X0) \wedge ((v1_xxreal_0 X0) \wedge ((v3_xxreal_0 X0) \wedge (v1_xreal_0 X0))) \quad (17)$$

Assume the following.

$$\forall X0.\forall X1.((v1_xreal_0 X0) \wedge (v1_xreal_0 X1)) \Rightarrow (v1_xreal_0 (k3_xcmplx_0 X0 X1)) \quad (18)$$

Assume the following.

$$v3_membered\ k1_numbers \quad (19)$$

Assume the following.

$$\forall X0.\forall X1.(((\neg v3_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \wedge ((\neg v3_xxreal_0 X1) \wedge (v1_xreal_0 X1))) \Rightarrow (\neg v3_xxreal_0 (k3_xcmplx_0 X0 X1)) \quad (20)$$

Assume the following.

$$\forall X0.\forall X1.(((\neg v2_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \wedge ((\neg v2_xxreal_0 X1) \wedge (v1_xreal_0 X1))) \Rightarrow (\neg v3_xxreal_0 (k3_xcmplx_0 X0 X1)) \quad (21)$$

Assume the following.

$$\forall X0.\forall X1.(((\neg v2_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \wedge ((\neg v3_xxreal_0 X1) \wedge (v1_xreal_0 X1))) \Rightarrow (\neg v2_xxreal_0 (k3_xcmplx_0 X1 X0)) \quad (22)$$

Assume the following.

$$\forall X0.\forall X1.((m1_subset_1 X0\ k1_numbers) \wedge (v1_xreal_0 X1)) \Rightarrow (m1_subset_1 (k8_real_1 X0 X1)\ k1_numbers) \quad (23)$$

Assume the following.

$$\forall X0.\forall X1.((m1_subset_1 X0\ k1_numbers) \wedge (m1_subset_1 X1\ k1_numbers)) \Rightarrow (m1_subset_1 (k4_power X0 X1)\ k1_numbers) \quad (24)$$

Assume the following.

$$\forall X0.\forall X1.((v1_xreal_0 X0) \wedge (v1_xreal_0 X1)) \Rightarrow (v1_xreal_0 (k3_power X0 X1)) \quad (25)$$

Assume the following.

$$\begin{aligned} & \forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\forall X2. \\ & (v1_xreal_0 X2) \Rightarrow (((\neg r1_xreal_0 X0 \ k6_numbers) \Rightarrow ((X2 = k3_power \\ & X0 X1) \Leftrightarrow (X2 = k9_prepower X0 X1))) \wedge (((X0 = k6_numbers) \Rightarrow ((r1_xreal_0 \\ & X1 \ k6_numbers) \vee ((X2 = k3_power X0 X1) \Leftrightarrow (X2 = k6_numbers)))))) \wedge ((v1_int_1 \\ & X1) \Rightarrow ((X2 = k3_power X0 X1) \Leftrightarrow (\exists X3.(v1_int_1 X3) \wedge ((X3 = X1) \wedge \\ & (X2 = k4_prepower X0 X3))))))))) \end{aligned} \quad (26)$$

Assume the following.

$$\forall X0.\forall X1.((m1_subset_1 X0 \ k1_numbers) \wedge (v1_xreal_0 X1)) \Rightarrow (k8_real_1 X0 X1 = k8_real_1 X1 X0) \quad (27)$$

Assume the following.

$$\forall X0.\forall X1.((v1_xcmplx_0 X0) \wedge (v1_xcmplx_0 X1)) \Rightarrow (k3_xcmplx_0 X0 X1 = k3_xcmplx_0 X1 X0) \quad (28)$$

Assume the following.

$$\forall X0.(v1_xboole_0 X0) \Rightarrow (v7_ordinal1 X0) \quad (29)$$

Assume the following.

$$\forall X0.(v3_membered X0) \Rightarrow (v1_membered X0) \quad (30)$$

Assume the following.

$$\forall X0.(((\neg v1_xboole_0 X0) \wedge ((v1_xxreal_0 X0) \wedge (\neg v3_xxreal_0 X0))) \Rightarrow ((v1_xxreal_0 X0) \wedge (v2_xxreal_0 X0))) \quad (31)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (v1_xxreal_0 X0) \quad (32)$$

Assume the following.

$$\forall X0.(((v1_xxreal_0 X0) \wedge (v2_xxreal_0 X0)) \Rightarrow (((\neg v1_xboole_0 X0) \wedge ((v1_xxreal_0 X0) \wedge (\neg v3_xxreal_0 X0)))) \quad (33)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (v1_xcmplx_0 X0) \quad (34)$$

Assume the following.

$$\forall X0.(v7_ordinal1 X0) \Rightarrow (v1_xreal_0 X0) \quad (35)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 \ k1_numbers) \Rightarrow (v1_xreal_0 X0) \quad (36)$$

Assume the following.

$$\forall X0.(v1_membered X0) \Rightarrow (\forall X1.(m1_subset_1 X1 X0) \Rightarrow (v1_xcmplx_0 X1)) \quad (37)$$

Theorem 1

$$\begin{aligned} & \forall X0.(m1_subset_1 X0 k1_numbers) \Rightarrow (\forall X1.(m1_subset_1 \\ & X1 k1_numbers) \Rightarrow (\forall X2.(m1_subset_1 X2 k1_numbers) \Rightarrow (((r1_xxreal_0 \\ & k6_numbers X1) \wedge (r1_xxreal_0 k6_numbers X2)) \Rightarrow ((r1_xxreal_0 X0 \\ & k6_numbers) \vee (k4_power (k8_real_1 X1 X2) X0 = k8_real_1 (k4_power \\ & X1 X0) (k4_power X2 X0)))))) \end{aligned}$$