

t12_abian

(TMMYMn9gPvxpzdt1e1csvyJj7iFXC2XsqgE)

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Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $v1_abian : \iota \Rightarrow o$ be given. Let $r1_xxreal_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $np_1 : \iota$ be given. Let $v1_xcmplx_0 : \iota \Rightarrow o$ be given. Let $k2_xcmplx_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k6_numbers : \iota$ be given. Let $v1_xxreal_0 : \iota \Rightarrow o$ be given. Let $v1_xreal_0 : \iota \Rightarrow o$ be given. Let $v1_int_1 : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k2_xcmplx_0 X0 k6_numbers = X0) \quad (1)$$

Assume the following.

$$\forall X0.(v7_ordinal1 X0) \Rightarrow ((\neg r1_xxreal_0 np_1 X0) \Rightarrow (X0 = k6_numbers)) \quad (2)$$

Assume the following.

$$\exists X0.(v1_xxreal_0 X0) \wedge ((v1_xcmplx_0 X0) \wedge ((v1_xreal_0 X0) \wedge ((v1_int_1 X0) \wedge (v1_abian X0)))) \quad (3)$$

Assume the following.

$$\forall X0.\forall X1.(((v1_int_1 X0) \wedge (v1_abian X0)) \wedge ((v1_int_1 X1) \wedge (\neg v1_abian X1))) \Rightarrow (\neg v1_abian (k2_xcmplx_0 X0 X1)) \quad (4)$$

Assume the following.

$$\forall X0.(v7_ordinal1 X0) \Rightarrow (v1_int_1 X0) \quad (5)$$

Theorem 1 $\forall X0.((v7_ordinal1 X0) \wedge (\neg v1_abian X0)) \Rightarrow (r1_xxreal_0 np_1 X0)$.