

t12_amistd_4 (TM- RiW1YgpteENRGS38UgegR6MSu5qTfNYxv)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_extpro_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_compos_1 : \iota \Rightarrow \iota$ be given. Let $v2_extpro_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $k4_amistd_4 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_partfun1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k4_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_amistd_4 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k2_extpro_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. \forall X2. ((X0 \in X1) \wedge (m1_subset_1 X1 (k1_zfmisc_1 X2))) \Rightarrow (m1_subset_1 X0 X2) \quad (1)$$

Assume the following.

$$\forall X0. \forall X1. \forall X2. ((\neg v1_setfam_1 X0) \wedge (((\neg v2_struct_0 X1) \wedge ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_extpro_1 X1 X0)))) \wedge (m1_subset_1 X2 (u1_compos_1 X1)))) \Rightarrow (m1_subset_1 (k4_amistd_4 X0 X1 X2) (k1_zfmisc_1 (u1_struct_0 X1))) \quad (2)$$

Assume the following.

$$\forall X0. \forall X1. \forall X2. \forall X3. \forall X4. (((\neg v1_setfam_1 X0) \wedge (((\neg v2_struct_0 X1) \wedge ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_extpro_1 X1 X0)))) \wedge (((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge ((v1_funct_1 X2) \wedge ((v5_funct_1 X2 (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 X2 (u1_struct_0 X1)))))) \wedge ((m1_subset_1 X3 (u1_struct_0 X1)) \wedge (m1_subset_1 X4 (k4_memstr_0 X0 X1 X3)))))) \Rightarrow ((v1_relat_1 (k1_amistd_4 X0 X1 X2 X3 X4)) \wedge ((v4_relat_1 (k1_amistd_4 X0 X1 X2 X3 X4) (u1_struct_0 X1)) \wedge ((v1_funct_1 (k1_amistd_4 X0 X1 X2 X3 X4)) \wedge ((v5_funct_1 (k1_amistd_4 X0 X1 X2 X3 X4) (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 (k1_amistd_4 X0 X1 X2 X3 X4) (u1_struct_0 X1)))))) \quad (3)$$

Assume the following.

$$\begin{aligned}
& \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((\neg v2_struct_0 X1) \wedge \\
& ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_extpro_1 X1 X0)))) \Rightarrow \\
& (\forall X2.(m1_subset_1 X2 (u1_compos_1 X1)) \Rightarrow (\forall X3.(m1_subset_1 \\
& X3 (k1_zfmisc_1 (u1_struct_0 X1))) \Rightarrow ((X3 = k4_amistd_4 X0 X1 X2) \Leftrightarrow \\
& (\forall X4.(m1_subset_1 X4 (u1_struct_0 X1)) \Rightarrow ((X4 \in X3) \Leftrightarrow (\neg \forall X5. \\
& ((v1_relat_1 X5) \wedge ((v4_relat_1 X5 (u1_struct_0 X1)) \wedge ((v1_funct_1 \\
& X5) \wedge ((v5_funct_1 X5 (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 X5 (u1_struct_0 \\
& X1)))))) \Rightarrow (\forall X6.(m1_subset_1 X6 (k4_memstr_0 X0 X1 X4)) \Rightarrow \\
& (k2_extpro_1 X0 X1 X2 (k1_amistd_4 X0 X1 X5 X4 X6) = k1_amistd_4 X0 \\
& X1 (k2_extpro_1 X0 X1 X2 X5) X4 X6)))))))))
\end{aligned} \tag{4}$$

Assume the following.

$$\begin{aligned}
& \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((v2_memstr_0 X1 X0) \wedge \\
& (l1_extpro_1 X1 X0)) \Rightarrow (\forall X2.(m1_subset_1 X2 (u1_compos_1 \\
& X1)) \Rightarrow ((v2_extpro_1 X2 X0 X1) \Leftrightarrow (\forall X3.((v1_relat_1 X3) \wedge ((\\
& v4_relat_1 X3 (u1_struct_0 X1)) \wedge ((v1_funct_1 X3) \wedge ((v5_funct_1 \\
& X3 (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 X3 (u1_struct_0 X1)))))) \Rightarrow \\
& (k2_extpro_1 X0 X1 X2 X3 = X3))))))
\end{aligned} \tag{5}$$

Assume the following.

$$\forall X0.(v1_xboole_0 X0) \Leftrightarrow (\forall X1. \neg X1 \in X0) \tag{6}$$

Theorem 1

$$\begin{aligned}
& \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((\neg v2_struct_0 X1) \wedge \\
& ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_extpro_1 X1 X0)))) \Rightarrow \\
& (\forall X2.(m1_subset_1 X2 (u1_compos_1 X1)) \Rightarrow ((v2_extpro_1 \\
& X2 X0 X1) \Rightarrow (v1_xboole_0 (k4_amistd_4 X0 X1 X2))))))
\end{aligned}$$