

t14_memstr_0
(TMMapm1zq3rRFQu1JXXoGPWopLHx6tpZfmL)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k9_subset_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k9_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k8_struct_0 : \iota \Rightarrow \iota$ be given. Let $k6_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k5_relat_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k3_xboole_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $l2_struct_0 : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0. \forall X1. (v1_relat_1 X1) \Rightarrow (k9_xtuple_0 (k5_relat_1 X1 X0) = k3_xboole_0 (k9_xtuple_0 X1) X0) \quad (1)$$

Assume the following.

$$\forall X0. \forall X1. \forall X2. (m1_subset_1 X2 (k1_zfmisc_1 X0)) \Rightarrow (k9_subset_1 X0 X1 X2 = k3_xboole_0 X1 X2) \quad (2)$$

Assume the following.

$$\forall X0. \forall X1. (l1_memstr_0 X1 X0) \Rightarrow (l2_struct_0 X1) \quad (3)$$

Assume the following.

$$\forall X0. (l2_struct_0 X0) \Rightarrow (m1_subset_1 (k8_struct_0 X0) (k1_zfmisc_1 (u1_struct_0 X0))) \quad (4)$$

Assume the following.

$$\begin{aligned} \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. (l1_memstr_0 X1 X0) \Rightarrow \\ (\forall X2. ((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge \\ ((v1_funct_1 X2) \wedge (v5_funct_1 X2 (k2_memstr_0 X0 X1)))))) \Rightarrow (k6_memstr_0 \\ X0 X1 X2 = k5_relat_1 X2 (k8_struct_0 X1)))) \quad (5) \end{aligned}$$

Theorem 1

$$\begin{aligned} & \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2.((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge \\ & ((v1_funct_1 X2) \wedge (v5_funct_1 X2 (k2_memstr_0 X0 X1)))))) \Rightarrow (k9_subset_1 \\ & (u1_struct_0 X1) (k9_xtuple_0 X2) (k8_struct_0 X1) = k9_xtuple_0 \\ & (k6_memstr_0 X0 X1 X2))) \end{aligned}$$