

t151_abc Miz_1

(TMcuJqzd4h7Y6Q7wg3TnDtr7M7BuyU3YuK1)

October 27, 2020

Let $v1_instalg1 : \iota \Rightarrow o$ be given. Let $v1_abc Miz_1 : \iota \Rightarrow o$ be given. Let $v3_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_msualg_1 : \iota \Rightarrow o$ be given. Let $m3_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $k2_zfmisc_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k2_abc Miz_1 : \iota$ be given. Let $k34_abc Miz_1 : \iota \Rightarrow \iota$ be given. Let $k64_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k65_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k38_abc Miz_1 : \iota \Rightarrow \iota$ be given. Let $k63_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k3_card_3 : \iota \Rightarrow \iota$ be given. Let $u3_msualg_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_msafree3 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k28_abc Miz_1 : \iota \Rightarrow \iota$ be given. Let $k56_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k41_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k42_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k62_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $m1_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k12_abc Miz_1 : \iota \Rightarrow \iota$ be given. Let $k2_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k1_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v9_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k40_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\begin{aligned}
 & \forall X0.((v1_instalg1 X0) \wedge ((v1_abc Miz_1 X0) \wedge ((v3_abc Miz_1 \\
 & X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.(m1_subset_1 X1 (k1_zfmisc_1 \\
 & (k38_abc Miz_1 X0))) \Rightarrow (\forall X2.((v1_funct_1 X2) \wedge (m1_subset_1 \\
 & X2 (k1_zfmisc_1 (k2_zfmisc_1 k2_abc Miz_1 (k34_abc Miz_1 X0)))))) \Rightarrow \\
 & (\forall X3.((v1_funct_1 X3) \wedge (m1_subset_1 X3 (k1_zfmisc_1 (k2_zfmisc_1 \\
 & k2_abc Miz_1 (k34_abc Miz_1 X0)))))) \Rightarrow (k63_abc Miz_1 X0 X3 (k63_abc Miz_1 \\
 & X0 X2 X1) = k63_abc Miz_1 X0 (k65_abc Miz_1 X0 X2 X3) X1)))
 \end{aligned} \tag{1}$$

Assume the following.

$$\begin{aligned}
 & \forall X0.((v1_instalg1 X0) \wedge ((v1_abc Miz_1 X0) \wedge ((v3_abc Miz_1 \\
 & X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.(m1_subset_1 X1 (k3_card_3 \\
 & (u3_msualg_1 X0 (k1_msafree3 X0 (k28_abc Miz_1 X0)))))) \Rightarrow (\forall X2. \\
 & ((v1_funct_1 X2) \wedge (m1_subset_1 X2 (k1_zfmisc_1 (k2_zfmisc_1 k2_abc Miz_1 \\
 & (k34_abc Miz_1 X0)))))) \Rightarrow (\forall X3.((v1_funct_1 X3) \wedge (m1_subset_1 \\
 & X3 (k1_zfmisc_1 (k2_zfmisc_1 k2_abc Miz_1 (k34_abc Miz_1 X0)))))) \Rightarrow \\
 & (k56_abc Miz_1 X0 X3 (k56_abc Miz_1 X0 X2 X1) = k56_abc Miz_1 X0 (k65_abc Miz_1 \\
 & X0 X2 X3) X1)))
 \end{aligned} \tag{2}$$

Assume the following.

$$\begin{aligned}
& \forall X0.((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 X0) \wedge ((v3_abcmiz_1 \\
& X0) \wedge (l1_msual\!g_1 X0)))) \Rightarrow (\forall X1.((v1_funct_1 X1) \wedge (m1_subset_1 \\
& X1 (k1_zfmisc_1 (k2_zfmisc_1 k2_abcmiz_1 (k34_abcmiz_1 X0)))))) \Rightarrow \\
& (\forall X2.(m3_abcmiz_1 X2 X0) \Rightarrow ((k41_abcmiz_1 X0 (k64_abcmiz_1 \\
& X0 X1 X2) = k63_abcmiz_1 X0 X1 (k41_abcmiz_1 X0 X2)) \wedge (k42_abcmiz_1 \\
& X0 (k64_abcmiz_1 X0 X1 X2) = k62_abcmiz_1 X0 X1 (k42_abcmiz_1 X0 X2))))))
\end{aligned} \tag{3}$$

Assume the following.

$$\begin{aligned}
& \forall X0.\forall X1.\forall X2.(((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 \\
& X0) \wedge ((v3_abcmiz_1 X0) \wedge (l1_msual\!g_1 X0)))) \wedge (((v1_funct_1 X1) \wedge \\
& (m1_subset_1 X1 (k1_zfmisc_1 (k2_zfmisc_1 k2_abcmiz_1 (k34_abcmiz_1 \\
& X0)))))) \wedge (m1_abcmiz_1 X2 X0 (k12_abcmiz_1 X0)))) \Rightarrow (k62_abcmiz_1 \\
& X0 X1 X2 = k56_abcmiz_1 X0 X1 X2)
\end{aligned} \tag{4}$$

Assume the following.

$$\begin{aligned}
& \forall X0.\forall X1.(((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 X0) \wedge \\
& ((v3_abcmiz_1 X0) \wedge (l1_msual\!g_1 X0)))) \wedge (m3_abcmiz_1 X1 X0)) \Rightarrow \\
& (k42_abcmiz_1 X0 X1 = k2_xtuple_0 X1)
\end{aligned} \tag{5}$$

Assume the following.

$$\begin{aligned}
& \forall X0.\forall X1.(((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 X0) \wedge \\
& ((v3_abcmiz_1 X0) \wedge (l1_msual\!g_1 X0)))) \wedge (m3_abcmiz_1 X1 X0)) \Rightarrow \\
& (k41_abcmiz_1 X0 X1 = k1_xtuple_0 X1)
\end{aligned} \tag{6}$$

Assume the following.

$$\begin{aligned}
& \forall X0.\forall X1.(((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 X0) \wedge \\
& ((v3_abcmiz_1 X0) \wedge (l1_msual\!g_1 X0)))) \wedge (m1_subset_1 X1 (u1_struct_0 \\
& X0))) \Rightarrow (\forall X2.(m1_abcmiz_1 X2 X0 X1) \Rightarrow (m1_subset_1 X2 (k3_card_3 \\
& (u3_msual\!g_1 X0 (k1_msafree3 X0 (k28_abcmiz_1 X0))))))
\end{aligned} \tag{7}$$

Assume the following.

$$\begin{aligned}
& \forall X0.\forall X1.\forall X2.(((v1_instal\!g_1 X0) \wedge ((v1_abcmiz_1 \\
& X0) \wedge ((v3_abcmiz_1 X0) \wedge (l1_msual\!g_1 X0)))) \wedge (((v1_funct_1 X1) \wedge \\
& (m1_subset_1 X1 (k1_zfmisc_1 (k2_zfmisc_1 k2_abcmiz_1 (k34_abcmiz_1 \\
& X0)))))) \wedge ((v1_funct_1 X2) \wedge (m1_subset_1 X2 (k1_zfmisc_1 (k2_zfmisc_1 \\
& k2_abcmiz_1 (k34_abcmiz_1 X0)))))) \Rightarrow ((v1_funct_1 (k65_abcmiz_1 \\
& X0 X1 X2)) \wedge (m1_subset_1 (k65_abcmiz_1 X0 X1 X2) (k1_zfmisc_1 (k2_zfmisc_1 \\
& k2_abcmiz_1 (k34_abcmiz_1 X0))))))
\end{aligned} \tag{8}$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. \forall X2. (((v1_instalg1\ X0) \wedge ((v1_abcmiz_1 \\ & X0) \wedge ((v3_abcmiz_1\ X0) \wedge (l1_msualg_1\ X0)))) \wedge (((v1_funct_1\ X1) \wedge \\ & (m1_subset_1\ X1\ (k1_zfmisc_1\ (k2_zfmisc_1\ k2_abcmiz_1\ (k34_abcmiz_1 \\ & X0)))) \wedge (m3_abcmiz_1\ X2\ X0))) \Rightarrow (m3_abcmiz_1\ (k64_abcmiz_1\ X0 \\ & X1\ X2)\ X0) \end{aligned} \quad (9)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. \forall X2. (((v1_instalg1\ X0) \wedge ((v1_abcmiz_1 \\ & X0) \wedge ((v3_abcmiz_1\ X0) \wedge (l1_msualg_1\ X0)))) \wedge (((v1_funct_1\ X1) \wedge \\ & (m1_subset_1\ X1\ (k1_zfmisc_1\ (k2_zfmisc_1\ k2_abcmiz_1\ (k34_abcmiz_1 \\ & X0)))) \wedge (m1_abcmiz_1\ X2\ X0\ (k12_abcmiz_1\ X0)))) \Rightarrow (m1_abcmiz_1 \\ & (k62_abcmiz_1\ X0\ X1\ X2)\ X0\ (k12_abcmiz_1\ X0)) \end{aligned} \quad (10)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. (((v1_instalg1\ X0) \wedge ((v1_abcmiz_1\ X0) \wedge \\ & ((v3_abcmiz_1\ X0) \wedge (l1_msualg_1\ X0)))) \wedge (m3_abcmiz_1\ X1\ X0)) \Rightarrow \\ & ((v9_abcmiz_1\ (k42_abcmiz_1\ X0\ X1)\ X0) \wedge (m1_abcmiz_1\ (k42_abcmiz_1 \\ & X0\ X1)\ X0\ (k12_abcmiz_1\ X0))) \end{aligned} \quad (11)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. (((v1_instalg1\ X0) \wedge ((v1_abcmiz_1\ X0) \wedge \\ & ((v3_abcmiz_1\ X0) \wedge (l1_msualg_1\ X0)))) \wedge (m3_abcmiz_1\ X1\ X0)) \Rightarrow \\ & (m1_subset_1\ (k41_abcmiz_1\ X0\ X1)\ (k1_zfmisc_1\ (k38_abcmiz_1 \\ & X0))) \end{aligned} \quad (12)$$

Assume the following.

$$\begin{aligned} & \forall X0. ((v1_instalg1\ X0) \wedge ((v1_abcmiz_1\ X0) \wedge (l1_msualg_1 \\ & X0))) \Rightarrow (m1_subset_1\ (k12_abcmiz_1\ X0)\ (u1_struct_0\ X0)) \end{aligned} \quad (13)$$

Assume the following.

$$\begin{aligned} & \forall X0. ((v1_instalg1\ X0) \wedge ((v1_abcmiz_1\ X0) \wedge ((v3_abcmiz_1 \\ & X0) \wedge (l1_msualg_1\ X0)))) \Rightarrow (\forall X1. ((v1_funct_1\ X1) \wedge (m1_subset_1 \\ & X1\ (k1_zfmisc_1\ (k2_zfmisc_1\ k2_abcmiz_1\ (k34_abcmiz_1\ X0)))))) \Rightarrow \\ & (\forall X2. (m3_abcmiz_1\ X2\ X0) \Rightarrow (k64_abcmiz_1\ X0\ X1\ X2 = k40_abcmiz_1 \\ & X0\ (k63_abcmiz_1\ X0\ X1\ (k41_abcmiz_1\ X0\ X2))\ (k62_abcmiz_1\ X0\ X1 \\ & (k42_abcmiz_1\ X0\ X2)))) \end{aligned} \quad (14)$$

Theorem 1

$$\begin{aligned} & \forall X0.((v1_instalg1\ X0)\wedge((v1_abcmiz_1\ X0)\wedge((v3_abcmiz_1 \\ & X0)\wedge(l1_msualg_1\ X0))))\Rightarrow(\forall X1.(m3_abcmiz_1\ X1\ X0)\Rightarrow(\forall X2. \\ & ((v1_funct_1\ X2)\wedge(m1_subset_1\ X2\ (k1_zfmisc_1\ (k2_zfmisc_1\ k2_abcmiz_1 \\ & (k34_abcmiz_1\ X0))))))\Rightarrow(\forall X3.((v1_funct_1\ X3)\wedge(m1_subset_1 \\ & X3\ (k1_zfmisc_1\ (k2_zfmisc_1\ k2_abcmiz_1\ (k34_abcmiz_1\ X0))))))\Rightarrow \\ & (k64_abcmiz_1\ X0\ X3\ (k64_abcmiz_1\ X0\ X2\ X1) = k64_abcmiz_1\ X0\ (k65_abcmiz_1 \\ & X0\ X2\ X3)\ X1)))) \end{aligned}$$