

t16_analmetr (TMazhSMn- hqLH7F9DAPxCdNrFvaSXGQghUXo)

October 27, 2020

Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v13_algstr_0 : \iota \Rightarrow o$ be given. Let $v2_rlvect_1 : \iota \Rightarrow o$ be given. Let $v3_rlvect_1 : \iota \Rightarrow o$ be given. Let $v4_rlvect_1 : \iota \Rightarrow o$ be given. Let $v5_rlvect_1 : \iota \Rightarrow o$ be given. Let $v6_rlvect_1 : \iota \Rightarrow o$ be given. Let $v7_rlvect_1 : \iota \Rightarrow o$ be given. Let $v8_rlvect_1 : \iota \Rightarrow o$ be given. Let $l1_rlvect_1 : \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $k2_diraf : \iota \Rightarrow \iota$ be given. Let $k2_analoaf : \iota \Rightarrow \iota$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $k2_zfmisc_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $g1_analoaf : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $l1_analoaf : \iota \Rightarrow o$ be given. Let $v1_analoaf : \iota \Rightarrow o$ be given. Let $u1_analoaf : \iota \Rightarrow \iota$ be given. Let $k1_analoaf : \iota \Rightarrow \iota$ be given. Let $k1_diraf : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. (m1_subset_1 X1 (k1_zfmisc_1 (k2_zfmisc_1 \\ & (k2_zfmisc_1 X0 X0) (k2_zfmisc_1 X0 X0)))) \Rightarrow (\forall X2. \forall X3. \\ & (g1_analoaf X0 X1 = g1_analoaf X2 X3) \Rightarrow ((X0 = X2) \wedge (X1 = X3))) \end{aligned} \quad (1)$$

Assume the following.

$$\forall X0. ((\neg v2_struct_0 X0) \wedge (l1_analoaf X0)) \Rightarrow ((\neg v2_struct_0 (k2_diraf X0)) \wedge (v1_analoaf (k2_diraf X0))) \quad (2)$$

Assume the following.

$$\begin{aligned} & \forall X0. ((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 \\ & X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge \\ & ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 \\ & X0)))))))))) \Rightarrow ((\neg v2_struct_0 (k2_analoaf X0)) \wedge (v1_analoaf (\\ & k2_analoaf X0))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} & \forall X0. (l1_analoaf X0) \Rightarrow (m1_subset_1 (u1_analoaf X0) (k1_zfmisc_1 \\ & (k2_zfmisc_1 (k2_zfmisc_1 (u1_struct_0 X0) (u1_struct_0 X0)) \\ & (k2_zfmisc_1 (u1_struct_0 X0) (u1_struct_0 X0)))) \end{aligned} \quad (4)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge (l1_analoaf X0)) \Rightarrow ((v1_analoaf (k2_diraf X0)) \wedge (l1_analoaf (k2_diraf X0))) \quad (5)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 X0)))))))))) \Rightarrow ((v1_analoaf (k2_analoaf X0)) \wedge (l1_analoaf (k2_analoaf X0))) \quad (6)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 X0)))))))))) \Rightarrow (m1_subset_1 (k1_analoaf X0) (k1_zfmisc_1 (k2_zfmisc_1 (u1_struct_0 X0) (u1_struct_0 X0)) (k2_zfmisc_1 (u1_struct_0 X0) (u1_struct_0 X0)))) \quad (7)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 X0)))))))))) \Rightarrow (k2_analoaf X0 = g1_analoaf (u1_struct_0 X0) (k1_analoaf X0)) \quad (8)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge (l1_analoaf X0)) \Rightarrow (k2_diraf X0 = g1_analoaf (u1_struct_0 X0) (k1_diraf (u1_struct_0 X0) (u1_analoaf X0))) \quad (9)$$

Assume the following.

$$\forall X0.(l1_analoaf X0) \Rightarrow ((v1_analoaf X0) \Rightarrow (X0 = g1_analoaf (u1_struct_0 X0) (u1_analoaf X0))) \quad (10)$$

Theorem 1

$$\forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 X0)))))))))) \Rightarrow (u1_struct_0 (k2_diraf (k2_analoaf X0)) = u1_struct_0 X0)$$