

t16_papdesaf
(TMHg8A87sgMCivVzuCn4E7iwCtnNxytxiNp)

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Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v13_algstr_0 : \iota \Rightarrow o$ be given. Let $v2_rlvect_1 : \iota \Rightarrow o$ be given. Let $v3_rlvect_1 : \iota \Rightarrow o$ be given. Let $v4_rlvect_1 : \iota \Rightarrow o$ be given. Let $v5_rlvect_1 : \iota \Rightarrow o$ be given. Let $v6_rlvect_1 : \iota \Rightarrow o$ be given. Let $v7_rlvect_1 : \iota \Rightarrow o$ be given. Let $v8_rlvect_1 : \iota \Rightarrow o$ be given. Let $l1_rlvect_1 : \iota \Rightarrow o$ be given. Let $v7_struct_0 : \iota \Rightarrow o$ be given. Let $v2_analoaf : \iota \Rightarrow o$ be given. Let $l1_analoaf : \iota \Rightarrow o$ be given. Let $k2_analoaf : \iota \Rightarrow \iota$ be given. Let $v7_aff_2 : \iota \Rightarrow o$ be given. Let $k2_diraf : \iota \Rightarrow \iota$ be given. Let $v6_papdesaf : \iota \Rightarrow o$ be given. Let $v4_aff_2 : \iota \Rightarrow o$ be given. Let $v1_diraf : \iota \Rightarrow o$ be given. Let $v1_analoaf : \iota \Rightarrow o$ be given. Let $l1_struct_0 : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0.((\neg v7_struct_0 X0) \wedge ((v2_analoaf X0) \wedge (l1_analoaf X0))) \Rightarrow ((v6_papdesaf X0) \Rightarrow (v4_aff_2 (k2_diraf X0))) \quad (1)$$

Assume the following.

$$\forall X0.((\neg v7_struct_0 X0) \wedge ((v1_diraf X0) \wedge (l1_analoaf X0))) \Rightarrow ((v4_aff_2 X0) \Rightarrow (v7_aff_2 X0)) \quad (2)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 X0)))))))))) \Rightarrow (\forall X1.((\neg v7_struct_0 X1) \wedge ((v2_analoaf X1) \wedge (l1_analoaf X1)))) \Rightarrow ((X1 = k2_analoaf X0) \Rightarrow (v6_papdesaf X1))) \quad (3)$$

Assume the following.

$$\forall X0.((\neg v7_struct_0 X0) \wedge ((v2_analoaf X0) \wedge (l1_analoaf X0))) \Rightarrow ((\neg v7_struct_0 (k2_diraf X0)) \wedge ((v1_analoaf (k2_diraf X0)) \wedge (v1_diraf (k2_diraf X0)))) \quad (4)$$

Assume the following.

$$\forall X0.(l1_analoaf X0) \Rightarrow (l1_struct_0 X0) \quad (5)$$

Assume the following.

$$\forall X0.((\neg v2_struct_0 X0) \wedge (l1_analoaf X0)) \Rightarrow ((v1_analoaf (k2_diraf X0)) \wedge (l1_analoaf (k2_diraf X0))) \quad (6)$$

Assume the following.

$$\forall X0.(l1_struct_0 X0) \Rightarrow ((v2_struct_0 X0) \Rightarrow (v7_struct_0 X0)) \quad (7)$$

Theorem 1

$$\begin{aligned} & \forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v2_rlvect_1 \\ & X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge ((v5_rlvect_1 X0) \wedge \\ & ((v6_rlvect_1 X0) \wedge ((v7_rlvect_1 X0) \wedge ((v8_rlvect_1 X0) \wedge (l1_rlvect_1 \\ & X0)))))))))) \Rightarrow (\forall X1.((\neg v7_struct_0 X1) \wedge ((v2_analoaf X1) \wedge \\ & (l1_analoaf X1))) \Rightarrow ((X1 = k2_analoaf X0) \Rightarrow (v7_aff_2 (k2_diraf X1)))) \end{aligned}$$