

t16_ranknull

(TMSaYt6X5Q8ZV5c4e2FayAKc8bQgq3LcVsc)

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Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v6_struct_0 : \iota \Rightarrow o$ be given. Let $v13_algstr_0 : \iota \Rightarrow o$ be given. Let $v33_algstr_0 : \iota \Rightarrow o$ be given. Let $v2_rlvect_1 : \iota \Rightarrow o$ be given. Let $v3_rlvect_1 : \iota \Rightarrow o$ be given. Let $v4_rlvect_1 : \iota \Rightarrow o$ be given. Let $v3_group_1 : \iota \Rightarrow o$ be given. Let $v5_group_1 : \iota \Rightarrow o$ be given. Let $v4_vectsp_1 : \iota \Rightarrow o$ be given. Let $v5_vectsp_1 : \iota \Rightarrow o$ be given. Let $l6_algstr_0 : \iota \Rightarrow o$ be given. Let $v8_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v9_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v10_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v11_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_matrlin : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_vectsp_9 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_vectsp_4 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_xboole_0 : \iota$ be given. Let $m1_vectsp_4 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k6_numbers : \iota$ be given. Let $k2_vectsp_4 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $l2_algstr_0 : \iota \Rightarrow o$ be given. Let $l5_algstr_0 : \iota \Rightarrow o$ be given. Let $l2_struct_0 : \iota \Rightarrow o$ be given. Let $l1_struct_0 : \iota \Rightarrow o$ be given. Let $l1_algstr_0 : \iota \Rightarrow o$ be given. Let $v7_vectsp_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $g1_vectsp_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $u1_algstr_0 : \iota \Rightarrow \iota$ be given. Let $u2_struct_0 : \iota \Rightarrow \iota$ be given. Let $u1_vectsp_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\begin{aligned}
& \forall X0. ((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v3_group_1 \\
& X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge \\
& ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))) \Rightarrow \\
& (\forall X1. ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 \\
& X1 X0) \wedge ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 \\
& X1 X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge \\
& (l1_vectsp_1 X1 X0)))))))))) \Rightarrow (\forall X2. (m1_vectsp_4 X2 X0 X1) \Rightarrow \\
& (k1_vectsp_4 X0 X2 = k1_vectsp_4 X0 X1)))
\end{aligned} \tag{1}$$

Assume the following.

$$\begin{aligned}
& \forall X0.((\neg v2_struct_0 X0) \wedge ((\neg v6_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v33_algstr_0 X0) \wedge ((v3_group_1 X0) \wedge ((v5_group_1 X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))))) \Rightarrow (\forall X1. \\
& ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 X1 X0) \wedge ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 X1 X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge ((v1_matrlin X1 X0) \wedge (l1_vectsp_1 X1 X0)))))))))) \Rightarrow ((k1_vectsp_9 X0 X1 = k6_numbers) \Leftrightarrow (k2_vectsp_4 X0 X1 = k1_vectsp_4 X0 X1)))
\end{aligned} \tag{2}$$

Assume the following.

$$k6_numbers = k1_xboole_0 \tag{3}$$

Assume the following.

$$\begin{aligned}
& \forall X0. \forall X1. (((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v3_group_1 X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))))) \wedge \\
& ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 X1 X0) \wedge ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 X1 X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge (l1_vectsp_1 X1 X0)))))))))) \Rightarrow (\forall X2. (m1_vectsp_4 X2 X0 X1) \Rightarrow ((\neg v2_struct_0 X2) \wedge ((v13_algstr_0 X2) \wedge ((v8_vectsp_1 X2 X0) \wedge ((v9_vectsp_1 X2 X0) \wedge ((v10_vectsp_1 X2 X0) \wedge ((v11_vectsp_1 X2 X0) \wedge ((v2_rlvect_1 X2) \wedge ((v3_rlvect_1 X2) \wedge ((v4_rlvect_1 X2) \wedge (l1_vectsp_1 X2 X0))))))))))
\end{aligned} \tag{4}$$

Assume the following.

$$\forall X0. (l6_algstr_0 X0) \Rightarrow ((l2_algstr_0 X0) \wedge (l5_algstr_0 X0)) \tag{5}$$

Assume the following.

$$\forall X0. (l2_struct_0 X0) \Rightarrow (l1_struct_0 X0) \tag{6}$$

Assume the following.

$$\forall X0. (l2_algstr_0 X0) \Rightarrow ((l2_struct_0 X0) \wedge (l1_algstr_0 X0)) \tag{7}$$

Assume the following.

$$\begin{aligned}
& \forall X0. \forall X1. (((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v3_group_1 X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))))) \wedge \\
& ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 X1 X0) \wedge ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 X1 X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge (l1_vectsp_1 X1 X0)))))))))) \Rightarrow ((v7_vectsp_1 (k1_vectsp_4 X0 X1) X0) \wedge (m1_vectsp_4 (k1_vectsp_4 X0 X1) X0 X1))
\end{aligned} \tag{8}$$

Assume the following.

$$\begin{aligned}
& \forall X0.((\neg v2_struct_0 X0) \wedge ((v13_algstr_0 X0) \wedge ((v3_group_1 \\
& X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge \\
& ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))))) \Rightarrow \\
& (\forall X1.((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 \\
& X1 X0) \wedge ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 \\
& X1 X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge \\
& (l1_vectsp_1 X1 X0)))))))))) \Rightarrow (k2_vectsp_4 X0 X1 = g1_vectsp_1 \\
& X0 (u1_struct_0 X1) (u1_algstr_0 X1) (u2_struct_0 X1) (u1_vectsp_1 \\
& X0 X1)))
\end{aligned} \tag{9}$$

Assume the following.

$$\begin{aligned}
& \forall X0. \forall X1. (((\neg v2_struct_0 X0) \wedge ((\neg v6_struct_0 X0) \wedge \\
& ((v13_algstr_0 X0) \wedge ((v33_algstr_0 X0) \wedge ((v3_group_1 X0) \wedge ((v5_group_1 \\
& X0) \wedge ((v4_vectsp_1 X0) \wedge ((v5_vectsp_1 X0) \wedge ((v2_rlvect_1 X0) \wedge \\
& ((v3_rlvect_1 X0) \wedge ((v4_rlvect_1 X0) \wedge (l6_algstr_0 X0)))))))))) \wedge \\
& ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v8_vectsp_1 X1 X0) \wedge \\
& ((v9_vectsp_1 X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 X1 \\
& X0) \wedge ((v2_rlvect_1 X1) \wedge ((v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge \\
& ((v1_matrlin X1 X0) \wedge (l1_vectsp_1 X1 X0)))))))))) \Rightarrow (\forall X2. \\
& (m1_vectsp_4 X2 X0 X1) \Rightarrow (v1_matrlin X2 X0))
\end{aligned} \tag{10}$$

Assume the following.

$$\begin{aligned}
& \forall X0. \forall X1. ((l1_struct_0 X0) \wedge (l1_vectsp_1 X1 X0)) \Rightarrow \\
& ((v7_vectsp_1 X1 X0) \Rightarrow (X1 = g1_vectsp_1 X0 (u1_struct_0 X1) (u1_algstr_0 \\
& X1) (u2_struct_0 X1) (u1_vectsp_1 X0 X1)))
\end{aligned} \tag{11}$$

Theorem 1

$$\begin{aligned}
& \forall X0. ((\neg v2_struct_0 X0) \wedge ((\neg v6_struct_0 X0) \wedge ((v13_algstr_0 \\
& X0) \wedge ((v33_algstr_0 X0) \wedge ((v2_rlvect_1 X0) \wedge ((v3_rlvect_1 X0) \wedge \\
& ((v4_rlvect_1 X0) \wedge ((v3_group_1 X0) \wedge ((v5_group_1 X0) \wedge ((v4_vectsp_1 \\
& X0) \wedge ((v5_vectsp_1 X0) \wedge (l6_algstr_0 X0)))))))))) \Rightarrow (\forall X1. \\
& ((\neg v2_struct_0 X1) \wedge ((v13_algstr_0 X1) \wedge ((v2_rlvect_1 X1) \wedge ((\\
& v3_rlvect_1 X1) \wedge ((v4_rlvect_1 X1) \wedge ((v8_vectsp_1 X1 X0) \wedge ((v9_vectsp_1 \\
& X1 X0) \wedge ((v10_vectsp_1 X1 X0) \wedge ((v11_vectsp_1 X1 X0) \wedge ((v1_matrlin \\
& X1 X0) \wedge (l1_vectsp_1 X1 X0)))))))))) \Rightarrow (k1_vectsp_9 X0 (k1_vectsp_4 \\
& X0 X1) = k1_xboole_0))
\end{aligned}$$