

t16_tex_4

(TMUjdrccZ3BAfdhoiFrpEqk787Tft4byBLD)

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Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $l1_pre_topc : \iota \Rightarrow o$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_tex_4 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_pre_topc : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_tex_4 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r1_tarski : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r1_xboole_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0. \forall X1. \forall X2. (\neg v1_xboole_0 X2) \Rightarrow (\neg (r1_tarski X2 X0) \wedge ((r1_tarski X2 X1) \wedge (r1_xboole_0 X0 X1))) \quad (1)$$

Assume the following.

$$\forall X0. \forall X1. r1_tarski X0 X0 \quad (2)$$

Assume the following.

$$\forall X0. (l1_pre_topc X0) \Rightarrow (\forall X1. (m1_subset_1 X1 (k1_zfmisc_1 (u1_struct_0 X0))) \Rightarrow ((v3_tex_4 X1 X0) \Leftrightarrow ((v1_tex_4 X1 X0) \wedge (\forall X2. (m1_subset_1 X2 (k1_zfmisc_1 (u1_struct_0 X0))) \Rightarrow (((v1_tex_4 X2 X0) \wedge (r1_tarski X1 X2)) \Rightarrow (X1 = X2))))))) \quad (3)$$

Assume the following.

$$\forall X0. (l1_pre_topc X0) \Rightarrow (\forall X1. (m1_subset_1 X1 (k1_zfmisc_1 (u1_struct_0 X0))) \Rightarrow ((v1_tex_4 X1 X0) \Leftrightarrow (\forall X2. (m1_subset_1 X2 (k1_zfmisc_1 (u1_struct_0 X0))) \Rightarrow (\neg (v3_pre_topc X2 X0) \wedge ((\neg r1_xboole_0 X1 X2) \wedge (\neg r1_tarski X1 X2))))))) \quad (4)$$

Assume the following.

$$\forall X0. \forall X1. (X0 = X1) \Leftrightarrow ((r1_tarski X0 X1) \wedge (r1_tarski X1 X0)) \quad (5)$$

Theorem 1

$$\forall X0. ((\neg v2_struct_0 X0) \wedge (l1_pre_topc X0)) \Rightarrow (\forall X1. ((\neg v1_xboole_0 X1) \wedge (m1_subset_1 X1 (k1_zfmisc_1 (u1_struct_0 X0)))) \Rightarrow (((v1_tex_4 X1 X0) \wedge (v3_pre_topc X1 X0)) \Rightarrow (v3_tex_4 X1 X0)))$$