

t17_memstr_0 (TMMawfD- pos2YQqVz3RPVmWFJ4mPkQmyHekf)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v5_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r1_tarski : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_funct_4 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0.((v1_relat_1 X0) \wedge (v1_funct_1 X0)) \Rightarrow (\forall X1.((v1_relat_1 X1) \wedge (v1_funct_1 X1)) \Rightarrow ((r1_tarski X0 X1) \Rightarrow (k1_funct_4 X1 X0 = X1))) \quad (1)$$

Assume the following.

$$\forall X0. \forall X1. \forall X2. \forall X3. \forall X4. ((\neg v1_setfam_1 X0) \wedge (((\neg v2_struct_0 X1) \wedge (v2_memstr_0 X1 X0) \wedge (v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \wedge ((v7_ordinal1 X2) \wedge (((v1_relat_1 X3) \wedge (v4_relat_1 X3 (u1_struct_0 X1)) \wedge (v1_funct_1 X3) \wedge (v5_funct_1 X3 (k2_memstr_0 X0 X1)))) \wedge ((v1_relat_1 X4) \wedge (v4_relat_1 X4 (u1_struct_0 X1)) \wedge (v1_funct_1 X4) \wedge (v5_funct_1 X4 (k2_memstr_0 X0 X1)) \wedge (v5_memstr_0 X4 X0 X1 X2)))))) \Rightarrow ((v1_relat_1 (k1_funct_4 X3 X4) \wedge ((v1_funct_1 (k1_funct_4 X3 X4) \wedge (v5_memstr_0 (k1_funct_4 X3 X4) X0 X1 X2)))))) \quad (2)$$

Theorem 1

$$\forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge ((v2_memstr_0 X1 X0) \wedge (v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow (\forall X2. (v7_ordinal1 X2) \Rightarrow (\forall X3. ((v1_relat_1 X3) \wedge (v4_relat_1 X3 (u1_struct_0 X1)) \wedge (v1_funct_1 X3) \wedge (v5_funct_1 X3 (k2_memstr_0 X0 X1)) \wedge (v5_memstr_0 X3 X0 X1 X2)))) \Rightarrow (\forall X4. ((v1_relat_1 X4) \wedge (v4_relat_1 X4 (u1_struct_0 X1)) \wedge (v1_funct_1 X4) \wedge (v5_funct_1 X4 (k2_memstr_0 X0 X1)))) \Rightarrow ((r1_tarski X3 X4) \Rightarrow (v5_memstr_0 X4 X0 X1 X2))))))$$