

t19_waybel34
(TMXd5pf6iS56sUob1pytjp9HPkoSiHTfd6o)

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Let $v2_setfam_1 : \iota \Rightarrow o$ be given. Let $k13_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k5_waybel34 : \iota \Rightarrow \iota$ be given. Let $k4_waybel34 : \iota \Rightarrow \iota$ be given. Let $k7_waybel34 : \iota \Rightarrow \iota$ be given. Let $k6_waybel34 : \iota \Rightarrow \iota$ be given. Let $k12_functor0 : \iota \Rightarrow \iota$ be given. Let $k15_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_altcat_1 : \iota \Rightarrow o$ be given. Let $v12_altcat_1 : \iota \Rightarrow o$ be given. Let $v1_altcat_2 : \iota \Rightarrow o$ be given. Let $l2_altcat_1 : \iota \Rightarrow o$ be given. Let $v8_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l2_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v21_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $g2_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $u1_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $u2_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v9_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v16_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $m2_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_altcat_1 : \iota \Rightarrow o$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $v6_altcat_1 : \iota \Rightarrow o$ be given. Let $v11_altcat_1 : \iota \Rightarrow o$ be given. Let $v2_yellow21 : \iota \Rightarrow o$ be given. Let $v12_functor0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Assume the following.

$$\begin{aligned} \forall X0. (\neg v2_setfam_1 X0) \Rightarrow & ((k15_functor0 (k4_waybel34 X0) \\ & (k5_waybel34 X0) (k6_waybel34 X0) = k7_waybel34 X0) \wedge (k15_functor0 \\ & (k5_waybel34 X0) (k4_waybel34 X0) (k7_waybel34 X0) = k6_waybel34 \\ & X0)) \end{aligned} \tag{1}$$

Assume the following.

$$\begin{aligned} \forall X0. ((\neg v2_struct_0 X0) \wedge ((v2_altcat_1 X0) \wedge ((v12_altcat_1 \\ X0) \wedge ((v1_altcat_2 X0) \wedge (l2_altcat_1 X0)))))) \Rightarrow (\forall X1. ((\neg \\ v2_struct_0 X1) \wedge ((v2_altcat_1 X1) \wedge ((v12_altcat_1 X1) \wedge ((v1_altcat_2 \\ X1) \wedge (l2_altcat_1 X1)))))) \Rightarrow (\forall X2. ((v8_functor0 X2 X0 X1) \wedge \\ (l2_functor0 X2 X0 X1)) \Rightarrow ((v21_functor0 X2 X0 X1) \Rightarrow (\forall X3. (\\ (v8_functor0 X3 X1 X0) \wedge (l2_functor0 X3 X1 X0)) \Rightarrow ((g2_functor0 X1 \\ X0 (u1_functor0 X1 X0 X3) (u2_functor0 X1 X0 X3) = k15_functor0 X0 \\ X1 X2) \Rightarrow (k13_functor0 X1 X0 X1 X3 X2 = k12_functor0 X1)))))) \end{aligned} \tag{2}$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v2_setfam_1 X0) \Rightarrow & ((v9_functor0 (k7_waybel34 X0) \\ & (k5_waybel34 X0) (k4_waybel34 X0)) \wedge ((v16_functor0 (k7_waybel34 \\ & X0) (k5_waybel34 X0) (k4_waybel34 X0)) \wedge (v21_functor0 (k7_waybel34 \\ & X0) (k5_waybel34 X0) (k4_waybel34 X0)))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v2_setfam_1 X0) \Rightarrow & ((v9_functor0 (k6_waybel34 X0) \\ & (k4_waybel34 X0) (k5_waybel34 X0)) \wedge ((v16_functor0 (k6_waybel34 \\ & X0) (k4_waybel34 X0) (k5_waybel34 X0)) \wedge (v21_functor0 (k6_waybel34 \\ & X0) (k4_waybel34 X0) (k5_waybel34 X0)))) \end{aligned} \quad (4)$$

Assume the following.

$$\begin{aligned} \forall X0.\forall X1.(((\neg v2_struct_0 X0) \wedge ((v2_altcat_1 X0) \wedge \\ ((v12_altcat_1 X0) \wedge (l2_altcat_1 X0)))) \wedge ((\neg v2_struct_0 X1) \wedge \\ ((v12_altcat_1 X1) \wedge (l2_altcat_1 X1)))) \Rightarrow (\forall X2.(m2_functor0 \\ X2 X0 X1) \Rightarrow (l2_functor0 X2 X0 X1)) \end{aligned} \quad (5)$$

Assume the following.

$$\forall X0.(l2_altcat_1 X0) \Rightarrow (l1_altcat_1 X0) \quad (6)$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v2_setfam_1 X0) \Rightarrow & ((v9_functor0 (k7_waybel34 X0) \\ & (k5_waybel34 X0) (k4_waybel34 X0)) \wedge ((v16_functor0 (k7_waybel34 \\ & X0) (k5_waybel34 X0) (k4_waybel34 X0)) \wedge (m2_functor0 (k7_waybel34 \\ & X0) (k5_waybel34 X0) (k4_waybel34 X0)))) \end{aligned} \quad (7)$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v2_setfam_1 X0) \Rightarrow & ((v9_functor0 (k6_waybel34 X0) \\ & (k4_waybel34 X0) (k5_waybel34 X0)) \wedge ((v16_functor0 (k6_waybel34 \\ & X0) (k4_waybel34 X0) (k5_waybel34 X0)) \wedge (m2_functor0 (k6_waybel34 \\ & X0) (k4_waybel34 X0) (k5_waybel34 X0)))) \end{aligned} \quad (8)$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v1_xboole_0 X0) \Rightarrow & ((\neg v2_struct_0 (k5_waybel34 X0)) \wedge \\ & ((v2_altcat_1 (k5_waybel34 X0)) \wedge ((v6_altcat_1 (k5_waybel34 \\ & X0)) \wedge ((v11_altcat_1 (k5_waybel34 X0)) \wedge ((v12_altcat_1 (k5_waybel34 \\ & X0)) \wedge ((v2_yellow21 (k5_waybel34 X0)) \wedge (l2_altcat_1 (k5_waybel34 \\ & X0)))))))))) \end{aligned} \quad (9)$$

Assume the following.

$$\begin{aligned} \forall X0. (\neg v1_xboole_0 X0) \Rightarrow & ((\neg v2_struct_0 (k4_waybel34 X0)) \wedge \\ & ((v2_altcat_1 (k4_waybel34 X0)) \wedge ((v6_altcat_1 (k4_waybel34 \\ X0)) \wedge ((v11_altcat_1 (k4_waybel34 X0)) \wedge ((v12_altcat_1 (k4_waybel34 \\ X0)) \wedge ((v2_yellow21 (k4_waybel34 X0)) \wedge (l2_altcat_1 (k4_waybel34 \\ X0)))))))))) \end{aligned} \quad (10)$$

Assume the following.

$$\forall X0. (\neg v2_setfam_1 X0) \Rightarrow (\neg v1_xboole_0 X0) \quad (11)$$

Assume the following.

$$\begin{aligned} \forall X0. \forall X1. (((\neg v2_struct_0 X0) \wedge ((v2_altcat_1 X0) \wedge \\ ((v12_altcat_1 X0) \wedge (l2_altcat_1 X0)))) \wedge ((\neg v2_struct_0 X1) \wedge \\ ((v12_altcat_1 X1) \wedge (l2_altcat_1 X1)))) \Rightarrow (\forall X2. (m2_functor0 \\ X2 X0 X1) \Rightarrow ((v8_functor0 X2 X0 X1) \wedge (v12_functor0 X2 X0 X1))) \end{aligned} \quad (12)$$

Assume the following.

$$\forall X0. (l2_altcat_1 X0) \Rightarrow (((\neg v2_struct_0 X0) \wedge (v12_altcat_1 X0)) \Rightarrow ((\neg v2_struct_0 X0) \wedge (v1_altcat_2 X0))) \quad (13)$$

Assume the following.

$$\begin{aligned} \forall X0. \forall X1. \forall X2. ((l1_altcat_1 X0) \wedge ((l1_altcat_1 \\ X1) \wedge (l2_functor0 X2 X0 X1))) \Rightarrow ((v9_functor0 X2 X0 X1) \Rightarrow (X2 = g2_functor0 \\ X0 X1 (u1_functor0 X0 X1 X2) (u2_functor0 X0 X1 X2))) \end{aligned} \quad (14)$$

Theorem 1

$$\begin{aligned} \forall X0. (\neg v2_setfam_1 X0) \Rightarrow & ((k13_functor0 (k5_waybel34 X0) \\ & (k4_waybel34 X0) (k5_waybel34 X0) (k7_waybel34 X0) (k6_waybel34 \\ X0) = k12_functor0 (k5_waybel34 X0)) \wedge (k13_functor0 (k4_waybel34 \\ X0) (k5_waybel34 X0) (k4_waybel34 X0) (k6_waybel34 X0) (k7_waybel34 \\ X0) = k12_functor0 (k4_waybel34 X0))) \end{aligned}$$