

t21_memstr_0

(TMZcMhCRaKVMJY9FZ8w5Ngwx38TKERcT4jW)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $k7_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k2_xcmplx_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k16_funcop_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_tarski : \iota \Rightarrow \iota$ be given. Let $k4_tarski : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_funct_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k7_nat_d : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k4_struct_0 : \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. k16_funcop_1 X0 X1 = k1_tarski (k4_tarski X0 X1) \quad (1)$$

Assume the following.

$$\forall X0. \forall X1. k1_funct_1 (k16_funcop_1 X0 X1) X0 = X1 \quad (2)$$

Assume the following.

$$\forall X0. (v7_ordinal1 X0) \Rightarrow (\forall X1. (v7_ordinal1 X1) \Rightarrow (k7_nat_d (k2_xcmplx_0 X0 X1) X1 = X0)) \quad (3)$$

Assume the following.

$$\forall X0. \forall X1. ((v7_ordinal1 X0) \wedge (v7_ordinal1 X1)) \Rightarrow (v7_ordinal1 (k2_xcmplx_0 X0 X1)) \quad (4)$$

Assume the following.

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2. (v7_ordinal1 X2) \Rightarrow (k7_memstr_0 X0 X1 X2 = k16_funcop_1 \\ & (k4_struct_0 X1) X2))) \end{aligned} \quad (5)$$

Theorem 1

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2. (v7_ordinal1 X2) \Rightarrow (\forall X3. (v7_ordinal1 X3) \Rightarrow (\\ & \forall X4. (v7_ordinal1 X4) \Rightarrow ((k7_memstr_0 X0 X1 (k2_xcmplx_0 \\ & X2 X4) = k7_memstr_0 X0 X1 (k2_xcmplx_0 X3 X4)) \Leftrightarrow (k7_memstr_0 X0 X1 \\ & X2 = k7_memstr_0 X0 X1 X3)))))) \end{aligned}$$