

t28_memstr_0
(TMX2rL48BKfdyKP9epN1j2oP2Xg63q6e6K9)

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Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_partfun1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r1_tarski : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_funct_4 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k7_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k5_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_funct_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k16_funcop_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k9_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k4_struct_0 : \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. k1_funct_1 (k16_funcop_1 X0 X1) X0 = X1 \quad (1)$$

Assume the following.

$$\begin{aligned} & \forall X0. ((v1_relat_1 X0) \wedge (v1_funct_1 X0)) \Rightarrow (\forall X1. ((\\ & v1_relat_1 X1) \wedge (v1_funct_1 X1)) \Rightarrow ((r1_tarski X0 X1) \Leftrightarrow ((r1_tarski \\ & (k9_xtuple_0 X0) (k9_xtuple_0 X1)) \wedge (\forall X2. (X2 \in k9_xtuple_0 \\ & X0) \Rightarrow (k1_funct_1 X0 X2 = k1_funct_1 X1 X2)))))) \end{aligned} \quad (2)$$

Assume the following.

$$\begin{aligned} & \forall X0. (v7_ordinal1 X0) \Rightarrow (\forall X1. (\neg v1_setfam_1 X1) \Rightarrow (\\ & \forall X2. ((\neg v2_struct_0 X2) \wedge ((v2_memstr_0 X2 X1) \wedge ((v3_memstr_0 \\ & X2 X1) \wedge (l1_memstr_0 X2 X1)))) \Rightarrow (\forall X3. ((v1_relat_1 X3) \wedge (\\ & (v4_relat_1 X3 (u1_struct_0 X2)) \wedge ((v1_funct_1 X3) \wedge (v5_funct_1 \\ & X3 (k2_memstr_0 X1 X2)))) \Rightarrow (k4_struct_0 X2 \in k9_xtuple_0 (k1_funct_4 \\ & X3 (k7_memstr_0 X1 X2 X0)))))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2. (v7_ordinal1 X2) \Rightarrow (k4_struct_0 X1 \in k9_xtuple_0 (k7_memstr_0 \\ & X0 X1 X2)))) \end{aligned} \quad (4)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. ((v1_relat_1 X1) \wedge (v1_funct_1 X1)) \Rightarrow (\forall X2. \\ & ((v1_relat_1 X2) \wedge (v1_funct_1 X2)) \Rightarrow ((X0 \in k9_xtuple_0 X1) \Rightarrow (k1_funct_1 \\ & (k1_funct_4 X2 X1) X0 = k1_funct_1 X1 X0))) \end{aligned} \quad (5)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. \forall X2. ((\neg v1_setfam_1 X0) \wedge (((\neg v2_struct_0 \\ & X1) \wedge ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 \\ & X0)))) \wedge (v7_ordinal1 X2))) \Rightarrow ((v1_relat_1 (k7_memstr_0 X0 X1 X2)) \wedge \\ & ((v4_relat_1 (k7_memstr_0 X0 X1 X2) (u1_struct_0 X1)) \wedge ((v1_funct_1 \\ & (k7_memstr_0 X0 X1 X2)) \wedge (v5_funct_1 (k7_memstr_0 X0 X1 X2) (k2_memstr_0 \\ & X0 X1)))))) \end{aligned} \quad (6)$$

Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. (((v1_relat_1 X0) \wedge (v1_funct_1 X0)) \wedge ((\\ & v1_relat_1 X1) \wedge (v1_funct_1 X1))) \Rightarrow ((v1_relat_1 (k1_funct_4 X0 \\ & X1)) \wedge (v1_funct_1 (k1_funct_4 X0 X1))) \end{aligned} \quad (7)$$

Assume the following.

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2. ((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge \\ & ((v1_funct_1 X2) \wedge (v5_funct_1 X2 (k2_memstr_0 X0 X1)))))) \Rightarrow (k5_memstr_0 \\ & X0 X1 X2 = k1_funct_1 X2 (k4_struct_0 X1))) \end{aligned} \quad (8)$$

Assume the following.

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge \\ & ((v2_memstr_0 X1 X0) \wedge ((v3_memstr_0 X1 X0) \wedge (l1_memstr_0 X1 X0)))) \Rightarrow \\ & (\forall X2. (v7_ordinal1 X2) \Rightarrow (k7_memstr_0 X0 X1 X2 = k16_funcop_1 \\ & (k4_struct_0 X1) X2))) \end{aligned} \quad (9)$$

Theorem 1

$$\begin{aligned} & \forall X0. (v7_ordinal1 X0) \Rightarrow (\forall X1. (\neg v1_setfam_1 X1) \Rightarrow (\\ & \forall X2. ((\neg v2_struct_0 X2) \wedge ((v2_memstr_0 X2 X1) \wedge ((v3_memstr_0 \\ & X2 X1) \wedge (l1_memstr_0 X2 X1)))) \Rightarrow (\forall X3. ((v1_relat_1 X3) \wedge (\\ & (v4_relat_1 X3 (u1_struct_0 X2)) \wedge ((v1_funct_1 X3) \wedge ((v5_funct_1 \\ & X3 (k2_memstr_0 X1 X2)) \wedge (v1_partfun1 X3 (u1_struct_0 X2)))))) \Rightarrow \\ & (\forall X4. ((v1_relat_1 X4) \wedge ((v4_relat_1 X4 (u1_struct_0 X2)) \wedge \\ & ((v1_funct_1 X4) \wedge (v5_funct_1 X4 (k2_memstr_0 X1 X2)))))) \Rightarrow ((r1_tarSKI \\ & (k1_funct_4 X4 (k7_memstr_0 X1 X2 X0)) X3) \Rightarrow (k5_memstr_0 X1 X2 X3 = \\ & X0)))))) \end{aligned}$$