

t3_gate_3 (TMLs-
FeVkR7Mwnpyoa8kN9iqvMwLSMoLQtJp)

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Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $k8_gate_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_gate_1 : \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. \forall X2. (\neg v1_xboole_0 (k8_gate_1 X0 X1 X2)) \Leftrightarrow ((\neg v1_xboole_0 X0) \wedge ((\neg v1_xboole_0 X1) \wedge (\neg v1_xboole_0 X2))) \quad (1)$$

Assume the following.

$$\forall X0. (\neg v1_xboole_0 X0) \Rightarrow (v1_xboole_0 (k1_gate_1 X0)) \quad (2)$$

Assume the following.

$$\forall X0. (v1_xboole_0 X0) \Rightarrow (\neg v1_xboole_0 (k1_gate_1 X0)) \quad (3)$$

