

t48_circtrm1

(TMaJ5P3udzPKY5nA2jSizW4ReLW9tb49tHa)

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Let $v2_struct_0 : \iota \Rightarrow o$ be given. Let $v11_struct_0 : \iota \Rightarrow o$ be given. Let $v2_msafree2 : \iota \Rightarrow o$ be given. Let $l1_msualg_1 : \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v4_msualg_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v4_msafree2 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l3_msualg_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r4_circtrm1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r3_circtrm1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k4_card_3 : \iota \Rightarrow \iota$ be given. Let $u3_msualg_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k3_relat_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_circuit2 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k6_circuit2 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $k5_facirc_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $np_1 : \iota$ be given. Let $k1_funct_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k12_card_3 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $r8_pboole : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v2_xxreal_0 : \iota \Rightarrow o$ be given. Let $m2_subset_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_numbers : \iota$ be given. Let $k5_numbers : \iota$ be given. Let $k4_ordinal1 : \iota$ be given. Assume the following.

$$\begin{aligned}
 & \forall X0. ((\neg v2_struct_0 X0) \wedge ((\neg v11_struct_0 X0) \wedge ((v2_msafree2 \\
 & \quad X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1. ((\neg v2_struct_0 X1) \wedge ((\neg \\
 & \quad v11_struct_0 X1) \wedge ((v2_msafree2 X1) \wedge (l1_msualg_1 X1)))) \Rightarrow (\forall X2. \\
 & \quad ((v1_relat_1 X2) \wedge (v1_funct_1 X2)) \Rightarrow (\forall X3. ((v1_relat_1 \\
 & \quad X3) \wedge (v1_funct_1 X3)) \Rightarrow (\forall X4. ((v4_msualg_1 X4 X0) \wedge ((v4_msafree2 \\
 & \quad X4 X0) \wedge (l3_msualg_1 X4 X0))) \Rightarrow (\forall X5. ((v4_msualg_1 X5 X1) \wedge \\
 & \quad ((v4_msafree2 X5 X1) \wedge (l3_msualg_1 X5 X1))) \Rightarrow (((r4_circtrm1 X0 \\
 & \quad X1 X2 X3 X4 X5) \wedge (r3_circtrm1 X0 X1 X2)) \Rightarrow (\forall X6. (m1_subset_1 \\
 & \quad X6 (k4_card_3 (u3_msualg_1 X1 X5))) \Rightarrow (\forall X7. (m1_subset_1 \\
 & \quad X7 (k4_card_3 (u3_msualg_1 X0 X4))) \Rightarrow ((X7 = k3_relat_1 X2 X6) \Rightarrow (k6_circuit2 \\
 & \quad X0 X4 X7 = k3_relat_1 X2 (k6_circuit2 X1 X5 X6))))))))))
 \end{aligned}
 \tag{1}$$

Assume the following.

$$\begin{aligned} \forall X0.((\neg v2_struct_0 X0) \wedge ((\neg v11_struct_0 X0) \wedge ((v2_msafree2 \\ X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.((v4_msualg_1 X1 X0) \wedge ((\\ v4_msafree2 X1 X0) \wedge (l3_msualg_1 X1 X0))) \Rightarrow (\forall X2.(m1_subset_1 \\ X2 (k4_card_3 (u3_msualg_1 X0 X1))) \Rightarrow ((v1_circuit2 X2 X0 X1) \Rightarrow (\forall X3. \\ (v7_ordinal1 X3) \Rightarrow (k5_facirc_1 X0 X1 X2 X3 = X2)))))) \end{aligned} \quad (2)$$

Assume the following.

$$\begin{aligned} \forall X0.((\neg v2_struct_0 X0) \wedge ((\neg v11_struct_0 X0) \wedge ((v2_msafree2 \\ X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.((v4_msualg_1 X1 X0) \wedge ((\\ v4_msafree2 X1 X0) \wedge (l3_msualg_1 X1 X0))) \Rightarrow (\forall X2.(m1_subset_1 \\ X2 (k4_card_3 (u3_msualg_1 X0 X1))) \Rightarrow (k5_facirc_1 X0 X1 X2 np_1 = \\ k6_circuit2 X0 X1 X2))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} \forall X0.((\neg v2_struct_0 X0) \wedge (l1_msualg_1 X0)) \Rightarrow (\forall X1. \\ ((v4_msualg_1 X1 X0) \wedge (l3_msualg_1 X1 X0)) \Rightarrow (\forall X2.(m1_subset_1 \\ X2 (k4_card_3 (u3_msualg_1 X0 X1))) \Rightarrow (\forall X3.(m1_subset_1 \\ X3 (k4_card_3 (u3_msualg_1 X0 X1))) \Rightarrow ((\forall X4.k1_funct_1 (\\ k12_card_3 (u3_msualg_1 X0 X1) X4) X2 = k1_funct_1 (k12_card_3 (\\ u3_msualg_1 X0 X1) X4) X3) \Rightarrow (r8_pboole (u1_struct_0 X0) X2 X3)))))) \end{aligned} \quad (4)$$

Assume the following.

$$\begin{aligned} ((v2_xxreal_0 np_1) \wedge (m2_subset_1 np_1 k1_numbers k5_numbers)) \wedge \\ ((m1_subset_1 np_1 k5_numbers) \wedge (m1_subset_1 np_1 k1_numbers)) \end{aligned} \quad (5)$$

Assume the following.

$$k5_numbers = k4_ordinal1 \quad (6)$$

Assume the following.

$$\begin{aligned} \forall X0.((\neg v2_struct_0 X0) \wedge ((\neg v11_struct_0 X0) \wedge ((v2_msafree2 \\ X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.((v4_msualg_1 X1 X0) \wedge ((\\ v4_msafree2 X1 X0) \wedge (l3_msualg_1 X1 X0))) \Rightarrow (\forall X2.(m1_subset_1 \\ X2 (k4_card_3 (u3_msualg_1 X0 X1))) \Rightarrow ((v1_circuit2 X2 X0 X1) \Leftrightarrow (r8_pboole \\ (u1_struct_0 X0) X2 (k6_circuit2 X0 X1 X2)))))) \end{aligned} \quad (7)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 k4_ordinal1) \Rightarrow (v7_ordinal1 X0) \quad (8)$$

Theorem 1

$$\begin{aligned} & \forall X0.((\neg v2_struct_0 X0) \wedge ((\neg v11_struct_0 X0) \wedge ((v2_msafree2 \\ & \quad X0) \wedge (l1_msualg_1 X0)))) \Rightarrow (\forall X1.((\neg v2_struct_0 X1) \wedge ((\neg \\ & \quad v11_struct_0 X1) \wedge ((v2_msafree2 X1) \wedge (l1_msualg_1 X1)))) \Rightarrow (\forall X2. \\ & \quad ((v1_relat_1 X2) \wedge (v1_funct_1 X2)) \Rightarrow (\forall X3.((v1_relat_1 \\ & \quad X3) \wedge (v1_funct_1 X3)) \Rightarrow (\forall X4.((v4_msualg_1 X4 X0) \wedge ((v4_msafree2 \\ & \quad X4 X0) \wedge (l3_msualg_1 X4 X0))) \Rightarrow (\forall X5.((v4_msualg_1 X5 X1) \wedge \\ & \quad ((v4_msafree2 X5 X1) \wedge (l3_msualg_1 X5 X1))) \Rightarrow (((r4_circuit1 X0 \\ & \quad X1 X2 X3 X4 X5) \wedge (r3_circuit1 X0 X1 X2)) \Rightarrow (\forall X6.(m1_subset_1 \\ & \quad X6 (k4_card_3 (u3_msualg_1 X1 X5))) \Rightarrow (\forall X7.(m1_subset_1 \\ & \quad X7 (k4_card_3 (u3_msualg_1 X0 X4))) \Rightarrow (((X7 = k3_relat_1 X2 X6) \wedge \\ & \quad v1_circuit2 X6 X1 X5)) \Rightarrow (v1_circuit2 X7 X0 X4)))))))))) \end{aligned}$$