

t48_glib_002
(TMVVRr4ZJoR16Ttpj4pf9qyBs1Nf6HkWxsv)

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Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k5_numbers : \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v1_finset_1 : \iota \Rightarrow o$ be given. Let $v1_glib_000 : \iota \Rightarrow o$ be given. Let $r5_glib_000 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_glib_002 : \iota \Rightarrow o$ be given. Let $v1_glib_002 : \iota \Rightarrow o$ be given. Let $v2_glib_002 : \iota \Rightarrow o$ be given. Assume the following.

$$\begin{aligned} \forall X0.((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 \\ X0) \wedge ((v1_finset_1 X0) \wedge (v1_glib_000 X0)))))) \Rightarrow (\forall X1.((v1_relat_1 \\ X1) \wedge ((v4_relat_1 X1 k5_numbers) \wedge ((v1_funct_1 X1) \wedge ((v1_finset_1 \\ X1) \wedge (v1_glib_000 X1)))))) \Rightarrow (((r5_glib_000 X0 X1) \wedge (v1_glib_002 \\ X0)) \Rightarrow (v1_glib_002 X1)) \end{aligned} \quad (1)$$

Assume the following.

$$\begin{aligned} \forall X0.((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 \\ X0) \wedge ((v1_finset_1 X0) \wedge (v1_glib_000 X0)))))) \Rightarrow (\forall X1.((v1_relat_1 \\ X1) \wedge ((v4_relat_1 X1 k5_numbers) \wedge ((v1_funct_1 X1) \wedge ((v1_finset_1 \\ X1) \wedge (v1_glib_000 X1)))))) \Rightarrow (((r5_glib_000 X0 X1) \wedge (v2_glib_002 \\ X0)) \Rightarrow (v2_glib_002 X1)) \end{aligned} \quad (2)$$

Assume the following.

$$\begin{aligned} \forall X0.((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 \\ X0) \wedge ((v1_finset_1 X0) \wedge ((v1_glib_000 X0) \wedge ((v1_glib_002 X0) \wedge \\ (v2_glib_002 X0)))))))) \Rightarrow ((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge \\ ((v1_funct_1 X0) \wedge ((v1_finset_1 X0) \wedge ((v1_glib_000 X0) \wedge (v3_glib_002 \\ X0))))))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} \forall X0.((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 \\ X0) \wedge ((v1_finset_1 X0) \wedge ((v1_glib_000 X0) \wedge (v3_glib_002 X0)))))) \Rightarrow \\ ((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 X0) \wedge \\ ((v1_finset_1 X0) \wedge ((v1_glib_000 X0) \wedge ((v1_glib_002 X0) \wedge (v2_glib_002 \\ X0)))))))) \end{aligned} \quad (4)$$

Theorem 1

$$\begin{aligned} \forall X0.((v1_relat_1 X0) \wedge ((v4_relat_1 X0 k5_numbers) \wedge ((v1_funct_1 \\ X0) \wedge ((v1_finset_1 X0) \wedge (v1_glib_000 X0)))))) \Rightarrow (\forall X1.((v1_relat_1 \\ X1) \wedge ((v4_relat_1 X1 k5_numbers) \wedge ((v1_funct_1 X1) \wedge ((v1_finset_1 \\ X1) \wedge (v1_glib_000 X1)))))) \Rightarrow (((r5_glib_000 X0 X1) \wedge (v3_glib_002 \\ X0)) \Rightarrow (v3_glib_002 X1)) \end{aligned}$$