

t4_abc Miz_a
(TMMcM3hJ1m5idm5RvDep9QYUDEFATGAKWVT)

October 27, 2020

Let $v1_instalg1 : \iota \Rightarrow o$ be given. Let $v1_abc Miz_1 : \iota \Rightarrow o$ be given. Let $l1_msualg_1 : \iota \Rightarrow o$ be given. Let $v1_abc Miz_a : \iota \Rightarrow o$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u4_struct_0 : \iota \Rightarrow \iota$ be given. Let $v2_abc Miz_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k20_abc Miz_1 : \iota$ be given. Let $r1_xboole_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $r1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_tarski : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k9_abc Miz_1 : \iota$ be given. Let $k10_abc Miz_1 : \iota$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $np_1 : \iota$ be given. Let $k1_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k2_msualg_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k1_card_1 : \iota \Rightarrow \iota$ be given. Let $k2_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k3_finseq_1 : \iota \Rightarrow \iota$ be given. Let $k1_msualg_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. (\neg(\neg r1_xboole_0 X0 X1) \wedge (\forall X2. \neg(X2 \in X0) \wedge (X2 \in X1))) \wedge (\neg(\exists X2. (X2 \in X0) \wedge (X2 \in X1))) \wedge (r1_xboole_0 X0 X1) \quad (1)$$

Assume the following.

$$r1_subset_1 (k2_tarski k9_abc Miz_1 k10_abc Miz_1) k20_abc Miz_1 \quad (2)$$

Assume the following.

$$\forall X0. \forall X1. ((\neg v1_xboole_0 X0) \wedge (\neg v1_xboole_0 X1)) \Rightarrow ((r1_subset_1 X0 X1) \Leftrightarrow (r1_xboole_0 X0 X1)) \quad (3)$$

Assume the following.

$$\forall X0. \forall X1. \neg v1_xboole_0 (k2_tarski X0 X1) \quad (4)$$

Assume the following.

$$\neg v1_xboole_0 k20_abc Miz_1 \quad (5)$$

Assume the following.

$$k10_abc Miz_1 = np_1 \quad (6)$$

Assume the following.

$$\forall X0.\forall X1.\forall X2.(X2 = k2_tarSKI X0 X1) \Leftrightarrow (\forall X3. (X3 \in X2) \Leftrightarrow ((X3 = X0) \vee (X3 = X1))) \quad (7)$$

Assume the following.

$$\begin{aligned} \forall X0.((v1_instalg1 X0) \wedge ((v1_abcmiz_1 X0) \wedge (l1_msualg_1 \\ X0))) \Rightarrow ((v1_abcmiz_a X0) \Leftrightarrow (\forall X1.(m1_subset_1 X1 (u4_struct_0 \\ X0))) \Rightarrow ((v2_abcmiz_1 X1 X0) \Rightarrow ((X1 \in k20_abcmiz_1) \wedge ((k1_xtuple_0 \\ X1 = k2_msualg_1 X0 X1) \wedge (k1_card_1 (k1_xtuple_0 (k2_xtuple_0 X1)) = \\ k3_finseq_1 (k1_msualg_1 X0 X1)))))) \end{aligned} \quad (8)$$

Assume the following.

$$\begin{aligned} \forall X0.((v1_instalg1 X0) \wedge ((v1_abcmiz_1 X0) \wedge (l1_msualg_1 \\ X0))) \Rightarrow (\forall X1.(m1_subset_1 X1 (u4_struct_0 X0))) \Rightarrow ((v2_abcmiz_1 \\ X1 X0) \Leftrightarrow ((X1 \neq k9_abcmiz_1) \wedge (X1 \neq k10_abcmiz_1))) \end{aligned} \quad (9)$$

Theorem 1

$$\begin{aligned} \forall X0.((v1_instalg1 X0) \wedge ((v1_abcmiz_1 X0) \wedge (l1_msualg_1 \\ X0))) \Rightarrow ((v1_abcmiz_a X0) \Rightarrow (\forall X1.(m1_subset_1 X1 (u4_struct_0 \\ X0))) \Rightarrow ((v2_abcmiz_1 X1 X0) \Leftrightarrow (X1 \in k20_abcmiz_1))) \end{aligned}$$