

t4_memstr_0

(TMbR4evBKHrpbAwh1eHi3dbpbKryNKYF9DV)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $r1_xboole_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_tarski : \iota \Rightarrow \iota$ be given. Let $k4_struct_0 : \iota \Rightarrow \iota$ be given. Let $k9_xtuple_0 : \iota \Rightarrow \iota$ be given. Let $k6_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\forall X0. \forall X1. (\neg X0 \in X1) \Rightarrow (r1_xboole_0 (k1_tarski X0) X1) \quad (1)$$

Assume the following.

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. (l1_memstr_0 X1 X0) \Rightarrow \\ & (\forall X2. ((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge \\ & ((v1_funct_1 X2) \wedge (v5_funct_1 X2 (k2_memstr_0 X0 X1)))))) \Rightarrow (\neg k4_struct_0 \\ & X1 \in k9_xtuple_0 (k6_memstr_0 X0 X1 X2)))) \end{aligned} \quad (2)$$

Theorem 1

$$\begin{aligned} & \forall X0. (\neg v1_setfam_1 X0) \Rightarrow (\forall X1. (l1_memstr_0 X1 X0) \Rightarrow \\ & (\forall X2. ((v1_relat_1 X2) \wedge ((v4_relat_1 X2 (u1_struct_0 X1)) \wedge \\ & ((v1_funct_1 X2) \wedge (v5_funct_1 X2 (k2_memstr_0 X0 X1)))))) \Rightarrow (r1_xboole_0 \\ & (k1_tarski (k4_struct_0 X1)) (k9_xtuple_0 (k6_memstr_0 X0 X1 X2)))) \end{aligned}$$