

t75_memstr_0 (TM-
NGR4wUMsCUe6mdAuhHtZyXhoarQniVbof)

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Let $v1_setfam_1 : \iota \Rightarrow o$ be given. Let $v2_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $l1_memstr_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_relat_1 : \iota \Rightarrow o$ be given. Let $v4_relat_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $u1_struct_0 : \iota \Rightarrow \iota$ be given. Let $v1_funct_1 : \iota \Rightarrow o$ be given. Let $v5_funct_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k2_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v1_finset_1 : \iota \Rightarrow o$ be given. Let $k11_memstr_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v2_relat_1 : \iota \Rightarrow o$ be given. Let $k8_card_3 : \iota \Rightarrow \iota$ be given. Let $v1_partfun1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0. \forall X1. (X0 \in X1) \Rightarrow (m1_subset_1 X0 X1) \quad (1)$$

Assume the following.

$$\begin{aligned} \forall X0. \forall X1. ((v1_relat_1 X1) \wedge ((v2_relat_1 X1) \wedge ((v4_relat_1 \\ X1 X0) \wedge (v1_funct_1 X1)))) \Rightarrow (\forall X2. ((v1_relat_1 X2) \wedge ((v4_relat_1 \\ X2 X0) \wedge ((v1_funct_1 X2) \wedge (v5_funct_1 X2 X1)))) \Rightarrow (X2 \in k8_card_3 \\ X1)) \end{aligned} \quad (2)$$

Assume the following.

$$\begin{aligned} \forall X0. \forall X1. ((\neg v1_setfam_1 X0) \wedge ((v2_memstr_0 X1 X0) \wedge \\ (l1_memstr_0 X1 X0))) \Rightarrow ((v1_relat_1 (k2_memstr_0 X0 X1)) \wedge ((v2_relat_1 \\ (k2_memstr_0 X0 X1)) \wedge ((v4_relat_1 (k2_memstr_0 X0 X1) (u1_struct_0 \\ X1)) \wedge ((v1_funct_1 (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 (k2_memstr_0 \\ X0 X1) (u1_struct_0 X1)))))) \end{aligned} \quad (3)$$

Assume the following.

$$\begin{aligned} \forall X0. \forall X1. ((\neg v1_setfam_1 X0) \wedge (l1_memstr_0 X1 X0)) \Rightarrow \\ ((v1_relat_1 (k2_memstr_0 X0 X1)) \wedge ((v4_relat_1 (k2_memstr_0 \\ X0 X1) (u1_struct_0 X1)) \wedge ((v1_funct_1 (k2_memstr_0 X0 X1)) \wedge (v1_partfun1 \\ (k2_memstr_0 X0 X1) (u1_struct_0 X1)))))) \end{aligned} \quad (4)$$

Assume the following.

$$\begin{aligned} \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((v2_memstr_0 X1 X0) \wedge \\ (l1_memstr_0 X1 X0)) \Rightarrow (k11_memstr_0 X0 X1 = ReplSep (toset (\lambda X2 : \\ \iota.m1_subset_1 X2 (k8_card_3 (k2_memstr_0 X0 X1)))) (\lambda X2 : \\ \iota.v1_finset_1 X2) (\lambda X2 : \iota.X2))) \end{aligned} \quad (5)$$

Assume the following.

$$\begin{aligned} \forall X0.\forall X1.((v1_relat_1 X1) \wedge ((v4_relat_1 X1 X0) \wedge \\ v1_funct_1 X1)) \Rightarrow (\forall X2.((v1_relat_1 X2) \wedge ((v1_funct_1 \\ X2) \wedge (v5_funct_1 X2 X1))) \Rightarrow ((v1_relat_1 X2) \wedge ((v4_relat_1 X2 X0) \wedge \\ (v1_funct_1 X2)))) \end{aligned} \quad (6)$$

Theorem 1

$$\begin{aligned} \forall X0.(\neg v1_setfam_1 X0) \Rightarrow (\forall X1.((v2_memstr_0 X1 X0) \wedge \\ (l1_memstr_0 X1 X0)) \Rightarrow (\forall X2.((v1_relat_1 X2) \wedge ((v4_relat_1 \\ X2 (u1_struct_0 X1)) \wedge ((v1_funct_1 X2) \wedge ((v5_funct_1 X2 (k2_memstr_0 \\ X0 X1)) \wedge (v1_finset_1 X2)))))) \Rightarrow (X2 \in k11_memstr_0 X0 X1)) \end{aligned}$$