

t7_gate_1
(TMXGHcHSoe5Uc73rZ4ZPAmiTfUvNaybvn9H)

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Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $k4_gate_1 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Assume the following.

$$\begin{aligned} & \forall X0. \forall X1. (\neg(\neg v1_xboole_0 (k4_gate_1 X0 X1)) \wedge ((\neg \\ & (\neg v1_xboole_0 X0) \wedge (v1_xboole_0 X1)) \wedge (\neg(v1_xboole_0 X0) \wedge (\neg v1_xboole_0 \\ & X1)))) \wedge (\neg(((\neg v1_xboole_0 X0) \wedge (v1_xboole_0 X1)) \vee ((v1_xboole_0 \\ & X0) \wedge (\neg v1_xboole_0 X1)))) \wedge (v1_xboole_0 (k4_gate_1 X0 X1))) \end{aligned} \tag{1}$$

Theorem 1 $\forall X0. v1_xboole_0 (k4_gate_1 X0 X0)$.