

t86_prepower (TMS- bEucsf85iSR2CfVbsp2mQ82odQN5USTU)

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Let $v1_xreal_0 : \iota \Rightarrow o$ be given. Let $r1_xxreal_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $np_1 : \iota$ be given. Let $k6_numbers : \iota$ be given. Let $k9_prepower : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $v3_xxreal_0 : \iota \Rightarrow o$ be given. Let $v2_xxreal_0 : \iota \Rightarrow o$ be given. Let $v1_xboole_0 : \iota \Rightarrow o$ be given. Let $k1_xboole_0 : \iota$ be given. Let $m2_subset_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_numbers : \iota$ be given. Let $k5_numbers : \iota$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v1_xcmplx_0 : \iota \Rightarrow o$ be given. Let $v1_xxreal_0 : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(\neg r1_xxreal_0 X0 X1) \wedge (\neg v3_xxreal_0 X1) \wedge (\neg v2_xxreal_0 X0)))) \quad (1)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\forall X2.(v1_xreal_0 X2) \Rightarrow (\neg(\neg r1_xxreal_0 X0 np_1) \wedge (\neg r1_xxreal_0 X1 X2) \wedge (r1_xxreal_0 (k9_prepower X0 X1) (k9_prepower X0 X2)))))) \quad (2)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow ((\neg r1_xxreal_0 X0 k6_numbers) \Rightarrow (k9_prepower X0 k6_numbers = np_1)) \quad (3)$$

Assume the following.

$$\forall X0.(v1_xboole_0 X0) \Rightarrow (X0 = k1_xboole_0) \quad (4)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(r1_xxreal_0 X0 X1) \wedge (\neg v2_xxreal_0 X1) \wedge (v2_xxreal_0 X0)))) \quad (5)$$

Assume the following.

$$((v2_xxreal_0 np_1) \wedge (m2_subset_1 np_1 k1_numbers k5_numbers)) \wedge ((m1_subset_1 np_1 k5_numbers) \wedge (m1_subset_1 np_1 k1_numbers)) \quad (6)$$

Assume the following.

$$k6_numbers = k1_xboole_0 \quad (7)$$

Assume the following.

$$\exists X0.(v1_xboole_0 X0) \wedge ((v1_xcmplx_0 X0) \wedge ((v1_xxreal_0 X0) \wedge (v1_xreal_0 X0))) \quad (8)$$

Assume the following.

$$\forall X0.((v1_xxreal_0 X0) \wedge (v2_xxreal_0 X0)) \Rightarrow ((\neg v1_xboole_0 X0) \wedge ((v1_xxreal_0 X0) \wedge (\neg v3_xxreal_0 X0))) \quad (9)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 k5_numbers) \Rightarrow (\neg v3_xxreal_0 X0) \quad (10)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 k1_numbers) \Rightarrow (v1_xreal_0 X0) \quad (11)$$

Theorem 1

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(\neg r1_xxreal_0 X0 np_1) \wedge ((\neg r1_xxreal_0 X1 k6_numbers) \wedge (r1_xxreal_0 (k9_prepower X0 X1) np_1))))$$