

t9_power (TMVx- FUEGVBPL8FuSCiVjkFBW4EdidYowNzd)

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Let $v1_xreal_0 : \iota \Rightarrow o$ be given. Let $k1_power : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $np_1 : \iota$ be given. Let $r1_xxreal_0 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $v3_xxreal_0 : \iota \Rightarrow o$ be given. Let $v2_xxreal_0 : \iota \Rightarrow o$ be given. Let $v1_xcmplx_0 : \iota \Rightarrow o$ be given. Let $k3_xcmplx_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $k6_numbers : \iota$ be given. Let $k2_prepower : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $m1_subset_1 : \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_xboole_0 : \iota$ be given. Let $k4_ordinal1 : \iota$ be given. Let $k4_xcmplx_0 : \iota \Rightarrow \iota$ be given. Let $m2_subset_1 : \iota \Rightarrow \iota \Rightarrow \iota \Rightarrow o$ be given. Let $k1_numbers : \iota$ be given. Let $k5_numbers : \iota$ be given. Let $np_0 : \iota$ be given. Let $k2_xcmplx_0 : \iota \Rightarrow \iota \Rightarrow \iota$ be given. Let $np_2 : \iota$ be given. Let $v1_int_1 : \iota \Rightarrow o$ be given. Let $v1_abian : \iota \Rightarrow o$ be given. Let $v6_membered : \iota \Rightarrow o$ be given. Let $k1_zfmisc_1 : \iota \Rightarrow \iota$ be given. Let $v7_ordinal1 : \iota \Rightarrow o$ be given. Let $v1_xxreal_0 : \iota \Rightarrow o$ be given. Let $v3_membered : \iota \Rightarrow o$ be given. Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (\neg(\neg r1_xxreal_0 X0 X1) \wedge ((\neg v3_xxreal_0 X1) \wedge (\neg v2_xxreal_0 X0)))) \quad (1)$$

Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k3_xcmplx_0 X0 k6_numbers = k6_numbers) \quad (2)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow ((r1_xxreal_0 k6_numbers X0) \Rightarrow (k2_prepower np_1 X0 = X0)) \quad (3)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (((r1_xxreal_0 X0 X1) \wedge (v2_xxreal_0 X0)) \Rightarrow (v2_xxreal_0 X1))) \quad (4)$$

Assume the following.

$$m1_subset_1 k1_xboole_0 k4_ordinal1 \quad (5)$$

Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k3_xcmplx_0 X0 (k4_xcmplx_0 np_1) = k4_xcmplx_0 X0) \quad (6)$$

Assume the following.

$$((v2_xxreal_0 np_1) \wedge (m2_subset_1 np_1 k1_numbers k5_numbers)) \wedge ((m1_subset_1 np_1 k5_numbers) \wedge (m1_subset_1 np_1 k1_numbers)) \quad (7)$$

Assume the following.

$$(m2_subset_1 np_0 k1_numbers k5_numbers) \wedge ((m1_subset_1 np_0 k5_numbers) \wedge (m1_subset_1 np_0 k1_numbers)) \quad (8)$$

Assume the following.

$$k4_xcmplx_0 np_0 = np_0 \quad (9)$$

Assume the following.

$$k2_xcmplx_0 (k4_xcmplx_0 np_1) np_2 = np_1 \quad (10)$$

Assume the following.

$$k2_xcmplx_0 (k4_xcmplx_0 np_1) np_1 = np_0 \quad (11)$$

Assume the following.

$$r1_xxreal_0 np_1 np_1 \quad (12)$$

Assume the following.

$$k6_numbers = k1_xboole_0 \quad (13)$$

Assume the following.

$$k5_numbers = k4_ordinal1 \quad (14)$$

Assume the following.

$$\forall X0.(v1_xcmplx_0 X0) \Rightarrow (k4_xcmplx_0 (k4_xcmplx_0 X0) = X0) \quad (15)$$

Assume the following.

$$\forall X0.\forall X1.(((v1_int_1 X0) \wedge (v1_abian X0)) \wedge (v1_int_1 X1)) \Rightarrow (v1_abian (k3_xcmplx_0 X1 X0)) \quad (16)$$

Assume the following.

$$v6_membered k4_ordinal1 \quad (17)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow ((v1_xcmplx_0 (k4_xcmplx_0 X0)) \wedge (v1_xreal_0 (k4_xcmplx_0 X0))) \quad (18)$$

Assume the following.

$$\forall X0.(v1_int_1 X0) \Rightarrow ((v1_xcmplx_0 (k4_xcmplx_0 X0)) \wedge (v1_int_1 (k4_xcmplx_0 X0))) \quad (19)$$

Assume the following.

$$\forall X0.((v1_int_1 X0) \wedge (v1_abian X0)) \Rightarrow (\neg v1_abian (k2_xcmplx_0 X0 \ np_1)) \quad (20)$$

Assume the following.

$$\forall X0.\forall X1.(((\neg v2_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \wedge ((\neg v3_xxreal_0 X1) \wedge (v1_xreal_0 X1))) \Rightarrow (\neg v2_xxreal_0 (k3_xcmplx_0 X0 X1)) \quad (21)$$

Assume the following.

$$\forall X0.((v1_int_1 X0) \wedge (\neg v1_abian X0)) \Rightarrow (\neg v1_abian (k2_xcmplx_0 X0 \ np_2)) \quad (22)$$

Assume the following.

$$\forall X0.((\neg v3_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \Rightarrow ((v1_xcmplx_0 (k4_xcmplx_0 X0)) \wedge (\neg v2_xxreal_0 (k4_xcmplx_0 X0))) \quad (23)$$

Assume the following.

$$\forall X0.((\neg v2_xxreal_0 X0) \wedge (v1_xreal_0 X0)) \Rightarrow ((v1_xcmplx_0 (k4_xcmplx_0 X0)) \wedge (\neg v3_xxreal_0 (k4_xcmplx_0 X0))) \quad (24)$$

Assume the following.

$$m1_subset_1 \ k5_numbers \ (k1_zfmisc_1 \ k1_numbers) \quad (25)$$

Assume the following.

$$\forall X0.(v7_ordinal1 X0) \Rightarrow (\forall X1.(v1_xreal_0 X1) \Rightarrow (((r1_xxreal_0 \ k6_numbers \ X1) \wedge (r1_xxreal_0 \ np_1 \ X0)) \Rightarrow (k1_power \ X0 \ X1 = k2_prepower \ X0 \ X1)) \wedge (\neg (\neg r1_xxreal_0 \ k6_numbers \ X1) \wedge (\neg v1_abian \ X0) \wedge (k1_power \ X0 \ X1 \neq k4_xcmplx_0 \ (k2_prepower \ X0 \ (k4_xcmplx_0 \ X1)))))) \quad (26)$$

Assume the following.

$$\forall X0.\forall X1.((v1_xxreal_0 X0) \wedge (v1_xxreal_0 X1)) \Rightarrow ((r1_xxreal_0 X0 \ X1) \vee (r1_xxreal_0 X1 \ X0)) \quad (27)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 (k1_zfmisc_1 k1_numbers)) \Rightarrow (v3_membered X0) \quad (28)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (v1_xxreal_0 X0) \quad (29)$$

Assume the following.

$$\forall X0.(v1_xreal_0 X0) \Rightarrow (v1_xcmplx_0 X0) \quad (30)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 k5_numbers) \Rightarrow (\neg v3_xxreal_0 X0) \quad (31)$$

Assume the following.

$$\forall X0.(v7_ordinal1 X0) \Rightarrow (v1_int_1 X0) \quad (32)$$

Assume the following.

$$\forall X0.(m1_subset_1 X0 k1_numbers) \Rightarrow (v1_xreal_0 X0) \quad (33)$$

Assume the following.

$$\forall X0.(v6_membered X0) \Rightarrow (\forall X1.(m1_subset_1 X1 X0) \Rightarrow (v7_ordinal1 X1)) \quad (34)$$

Assume the following.

$$\forall X0.(v3_membered X0) \Rightarrow (\forall X1.(m1_subset_1 X1 X0) \Rightarrow (v1_xreal_0 X1)) \quad (35)$$

Theorem 1 $\forall X0.(v1_xreal_0 X0) \Rightarrow (k1_power\ np_1 X0 = X0)$.